

LI8M

General

The logic input LI8M allows to feed up to 8 logical signals into compatible signal converters or data loggers (like SICOLOG, SICO3, SICO2, CFDL1, DL16CAN) via the LIO connection.

The switching voltage is hereby +2.5 V DC, and the input signals can have a voltage level of up to ± 100 V DC.

The pull up/down resistors are common for four input signals (1...4 and 5...8). They are dependent on the respective switch state: the input signals are either connected together over 200 k Ω resistors (3 o'clock position), or connected to 5 V DC over a 100 k Ω resistor (2 o'clock position), or connected to 0 V over a 100 k Ω resistor (4 o'clock position).



Figure 1: Logic Input LI8M.

Pin Assignment

The sockets and plugs of the LI8M are manufactured by Binder and parts of [Binder Series 719](#).

IN1...4 / IN5...8: This socket provides the inputs for the logical input signals.

Pin	Assignment
1	Channel 1 (or channel 5)
2	Channel 2 (or channel 6)
3	Channel 3 (or channel 7)
4	Channel 4 (or channel 8)
5	Ground (with protective resistor $R = 200 \Omega$)

LIO: This plug provides the connection to the I²C bus of the signal converter or data logger.

Pin	Assignment
1	Supplying voltage U_B (6...16 V DC)
2	Ground
3	SDA (serial data line)
4	SCL (serial clock line)

Technical Data

Box dimensions:	62 mm x 26 mm x 8 mm
Typical weight:	36 g
Typical current consumption:	5 mA